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FACSIMILE COVER SHEET

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Facsimile Number: (703) 746-4000	Transmission Date:	March 28, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yeo, *et al.* Docket No.: TSM03-0556
Serial No: 10/627,218 Art Unit: 2831
Date Filed: July 25, 2003
Title: Capacitor with Enhanced Performance and Method of Manufacture

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I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (703) 746-4000 on the date shown above:

- Certification of Facsimile Transmission (1 page)
- Request for Confirmation of Information Disclosure Statement (1 page)
- Copy of PTO/SB/08A as filed October 31, 2003 (1 page)

Respectfully submitted,

Kristin R. Hayes
Legal Assistant

Confirmation Respectfully Requested

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yeo, *et al.* Docket No.: TSM03-0556
Serial No.: 10/627,218 Art Unit: 2831
Filed: July 25, 2003 Examiner: Ha, Nguyen T.
For: Capacitor with Enhanced Performance and Method of Manufacture

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Request For Confirmation of Information Disclosure Statement

Dear Sir:

The patent application for the above-referenced patent application was allowed on February 10, 2005. An Information Disclosure Statement (copy attached) was filed by Applicant on October 31, 2003. However, Applicant did not receive confirmation of consideration of the Information Disclosure Statement by the Examiner with the Notice of Allowance or any other prior letter mailed from the USPTO. Applicant kindly requests that the Office forward the initialed copy of the PTO/SB/08A.

Respectfully submitted,

March 28, 2005

Date

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MAR 28 2005

PTO/SB/08A (05-03)

Approved for use through 05/31/2003. CMB 0851-0031
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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

1

of

1

Complete if Known

Application Number	10/627,218
Filing Date	July 25, 2003
First Named Inventor	Yeo, et al.
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	TSM03-0556

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	A	US-6,258,884 B1	07-10-2001	Reinberg	
	B	US-6,358,791 B1	03-19-2002	Hsu, et al.	
	C	US-2002/0153549 A1	10-24-2002	Leibowitz, et al.	
	D	US-6,475,838 B1	11-05-2002	Bryant, et al.	
	E	US-2003/0030091 A1	02-13-2003	Bulsara, et al.	
	F	US-6,524,805 B2	02-25-2003	Yamamichi, et al.	
	G	US-6,558,998 B2	05-06-2003	Baileville, et al.	
		US-			
		US-			
		US-			
		US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ^o
		Country Code ³ - Number ⁴ - Kind Code ⁶ (if known)				
	H	WO 03/017336 A2	02-27-2003	Amberwave Systems Corporation		

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issues number(s), publisher, city and/or country where published.	T ²
	I	WANG, L.K., et al., "On-Chip Decoupling Capacitor Design to Reduce Switching-Noise-Induced Instability in CMOS/SOI VLSI," Proceedings of the 1995 IEEE International SOI Conference, Oct. 1995, pp. 100-101.	
	J	YEOH, J.C., et al., "MOS Gated Si/SiGe Quantum Wells Formed by Anodic Oxidation," Semicond. Sci. Technol. (1998), Vol. 13, pp. 1442-1445, IOP Publishing Ltd., UK.	
	K	CAVASSILAS, N., et al., "Capacitance-Voltage Characteristics of Metal-Oxide-Strained Semiconductor Si/SiGe Heterostructures," Nanotech 2002, Vol. 1, pp. 600-603.	

Examiner Signature	Date Considered
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